Amendments to the Specification:

Please replace the paragraph at page 2, lines 11-14 with the following amended paragraph:

The request-reordering logic 110 reorders requested priorities of the master blocks and outputs a request-reordering signal so that the priorities can be in accordance with priority information of the master blocks stored in a <u>programmable priority register, (HPRIF)</u> ~~register~~. Here, the priorities of the master blocks can be programmed into the HPRIF register.